

10/058319



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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

JCH/ase

APPL NUM 10058319	FILING DATE 01/30/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER L. THOM
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****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A CON OF 09/768,451 01/25/2001
WHICH IS A CON OF 09/449,834 11/26/1999 PAT 6,342,726
WHICH IS A CON OF 08/822,933 03/21/1997 ABN

Lum Thoi

**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN 8-66637 03/22/1996

LT.

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
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Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's Initials <i>LT</i>	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 501.35286C14
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TITLE : Semiconductor device and manufacturing method thereof

U.S. DEPT. OF COMM. / PAT. & TM. PTO-435 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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